

Docket No.: GR 98 P 1048

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : ARMIN PITZER ET AL.
Filed : Concurrently herewith
Title : DIGITALLY CONTROLLED CIRCUIT FOR REDUCING THE
PHASE MODULATION OF A SIGNAL



INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 5,602,882 (Co et al.), dated February 11, 1997, as mentioned on page 2 of the specification;

U.S. Patent No. 5,502,750 (Co et al.), dated March 26, 1996, as mentioned on page 2 of the specification;

U.S. Patent No. 5,493,243 (Ghoshal), dated February 20, 1996, as mentioned on page 2 of the specification;

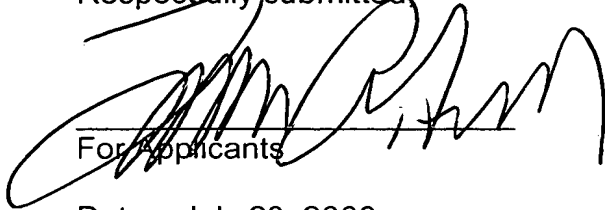
Published European Patent Application No. EP 0 692 878 A2 (Götz), dated January 17, 1996, pertains to a transmitting system;

German Published, Non-Prosecuted Application No. DE 39 42 883 A1 (Urbansky), dated June 27, 1991, pertains to a circuitry for the bit-rate matching;

Published International Application No. WO 96/28889 (Wilson et al.), dated
September 19, 1996;

If no translation of pertinent portions of any foreign language patents or publications
mentioned above is included with the aforementioned copies of those applications,
patents and/or publications, it is because no existing translation is readily available to
the applicant.

Respectfully submitted,



For Applicants

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